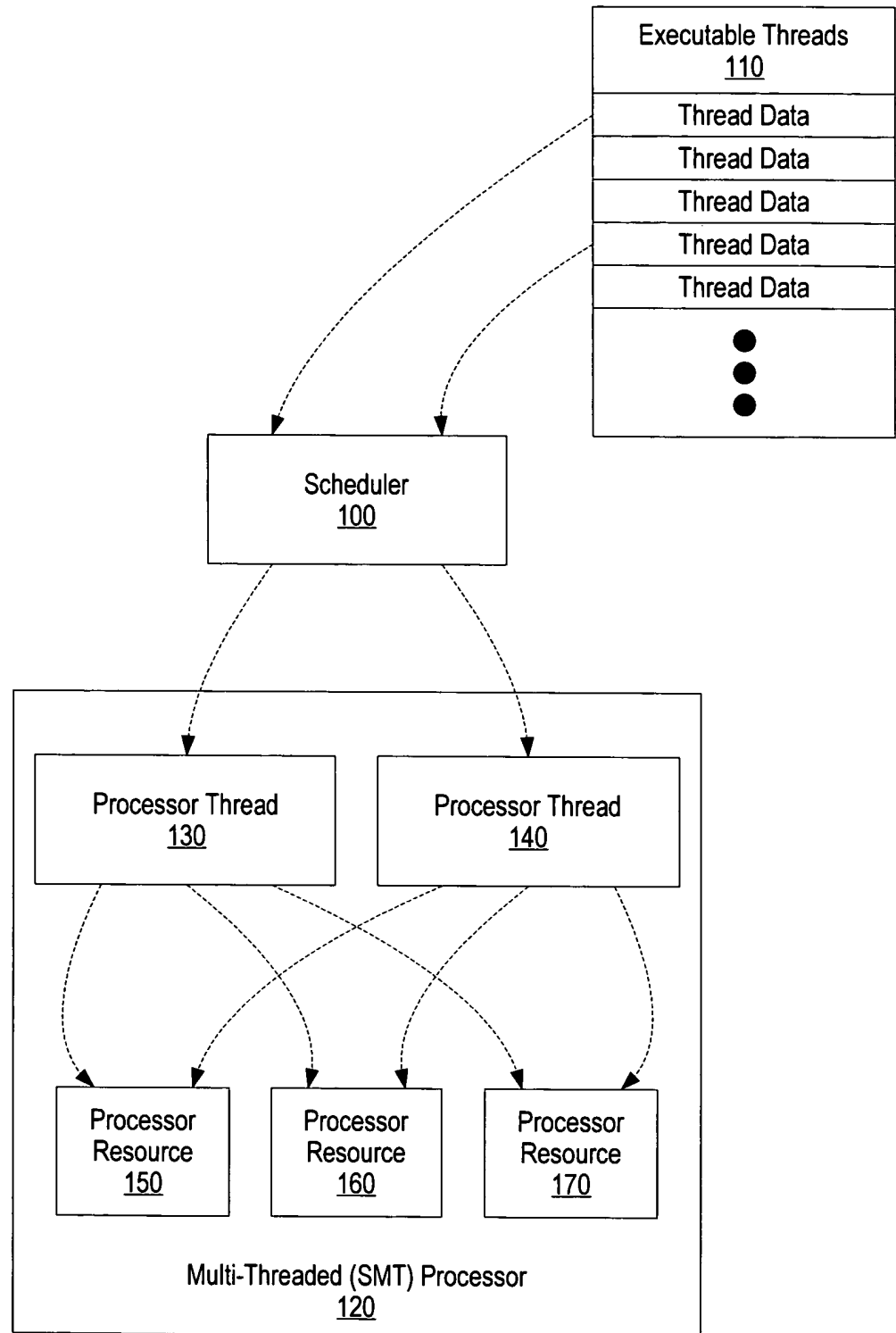
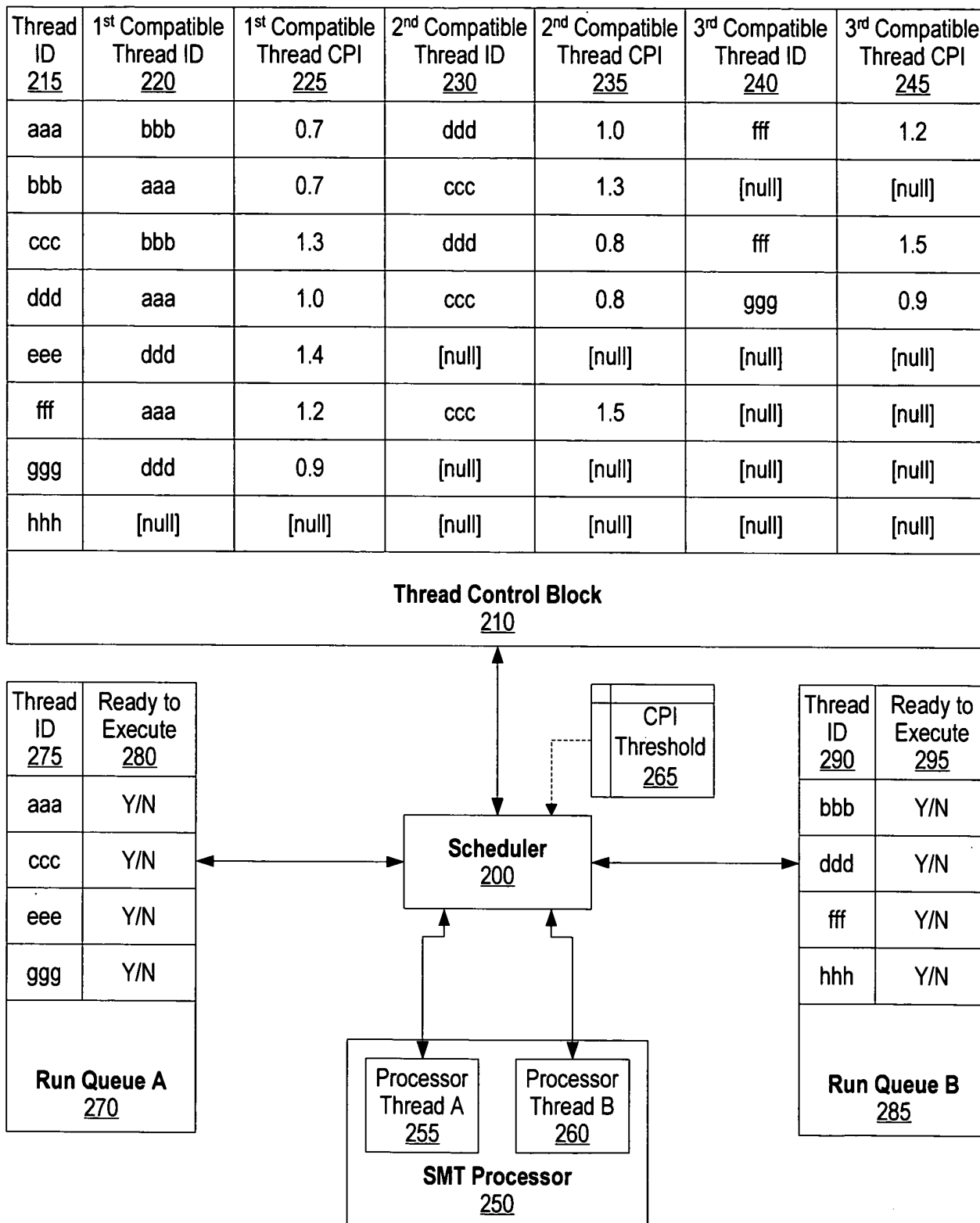


## System and Method for CPI Load Balancing in SMT Environments

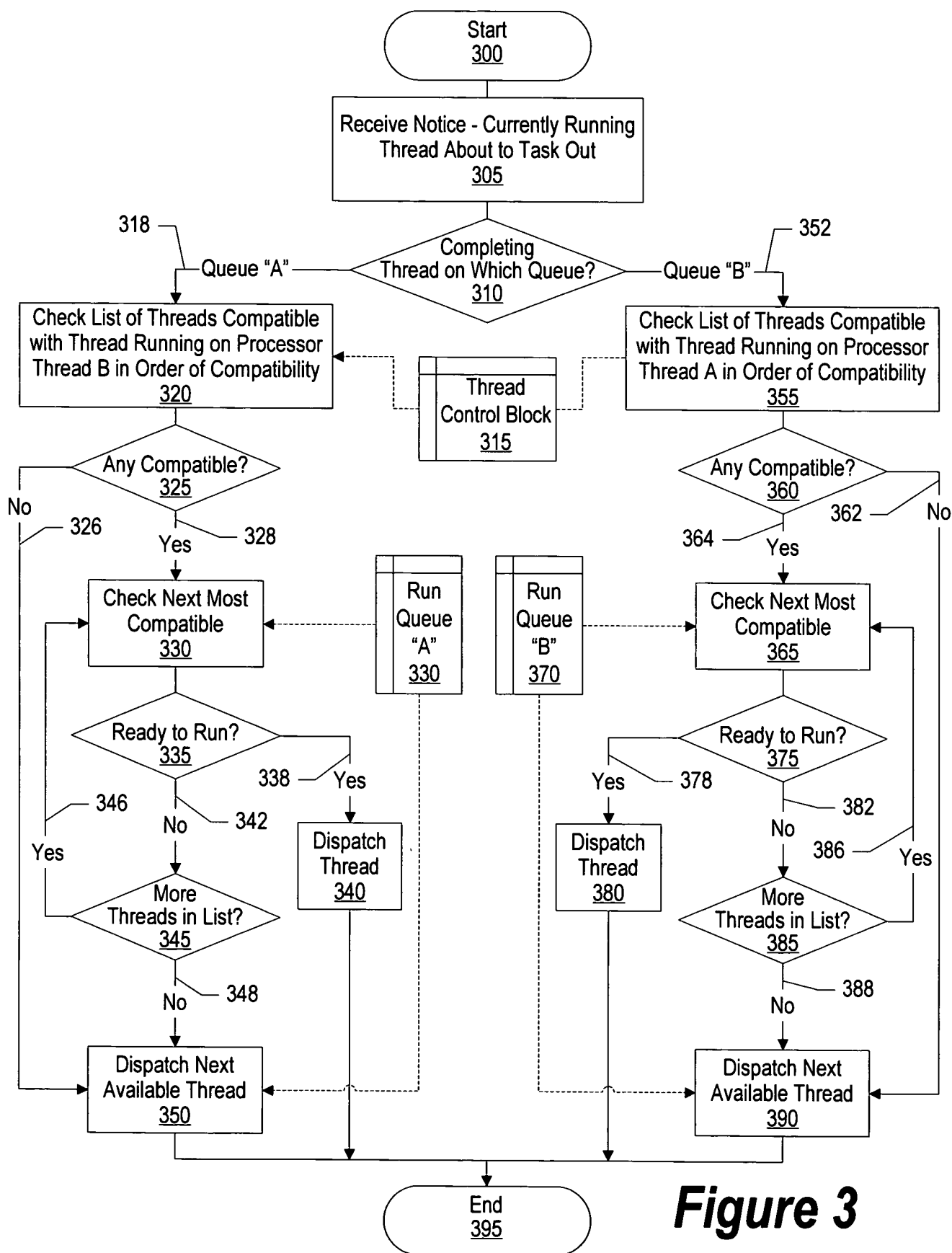
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**Figure 1**

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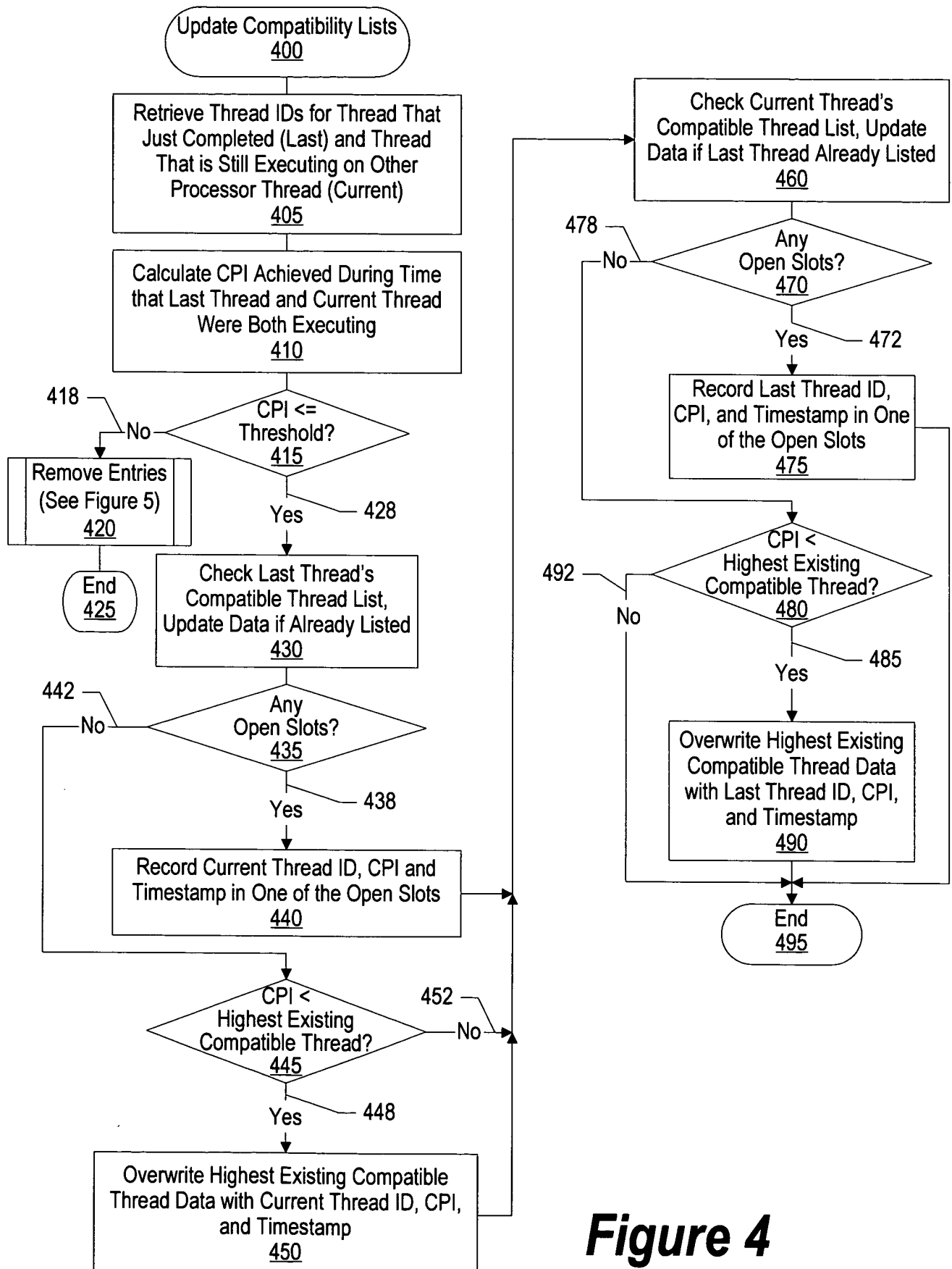
**Figure 2**

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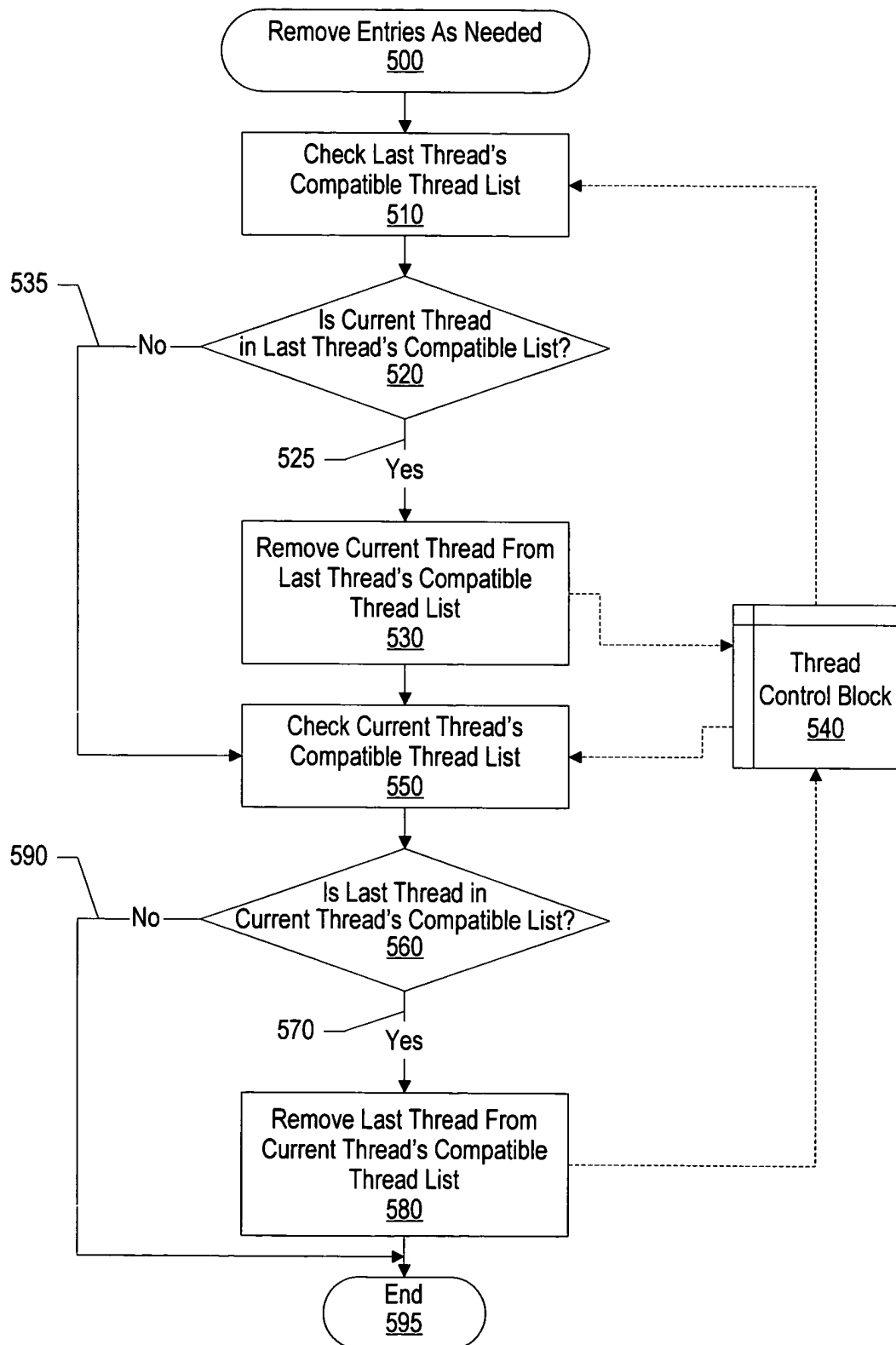
**Figure 3**

## System and Method for CPI Load Balancing in SMT Environments

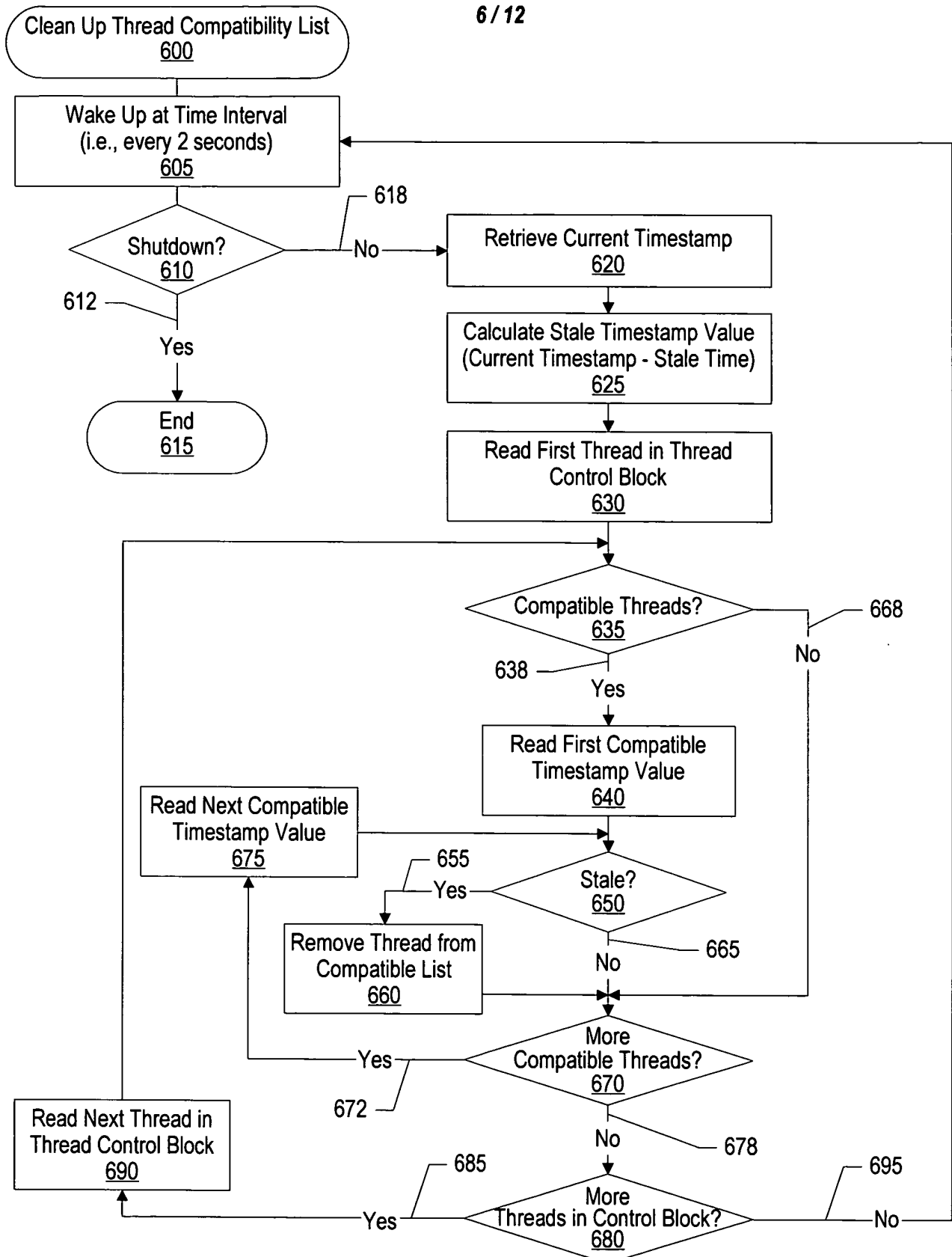
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**Figure 4**

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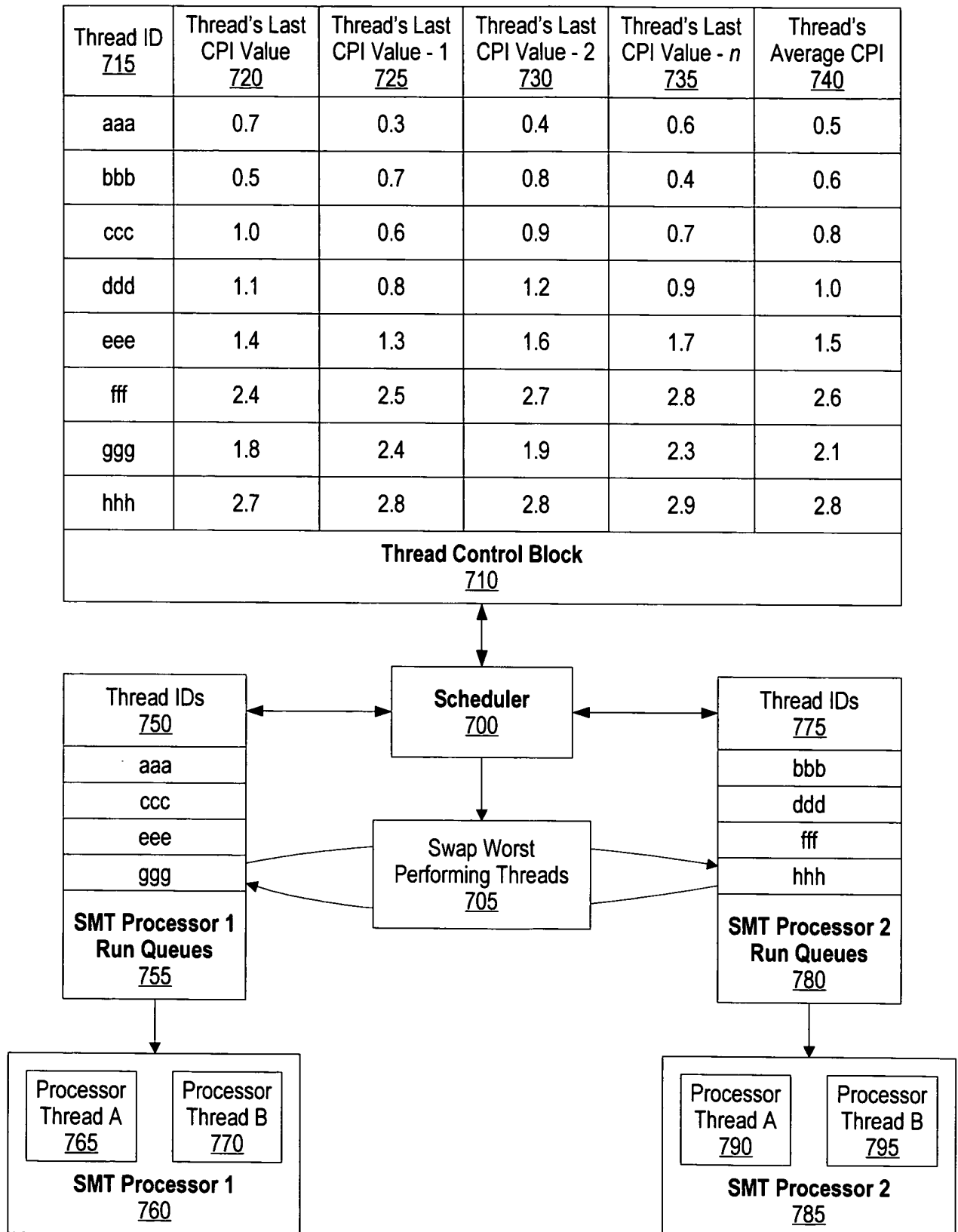
**Figure 5**

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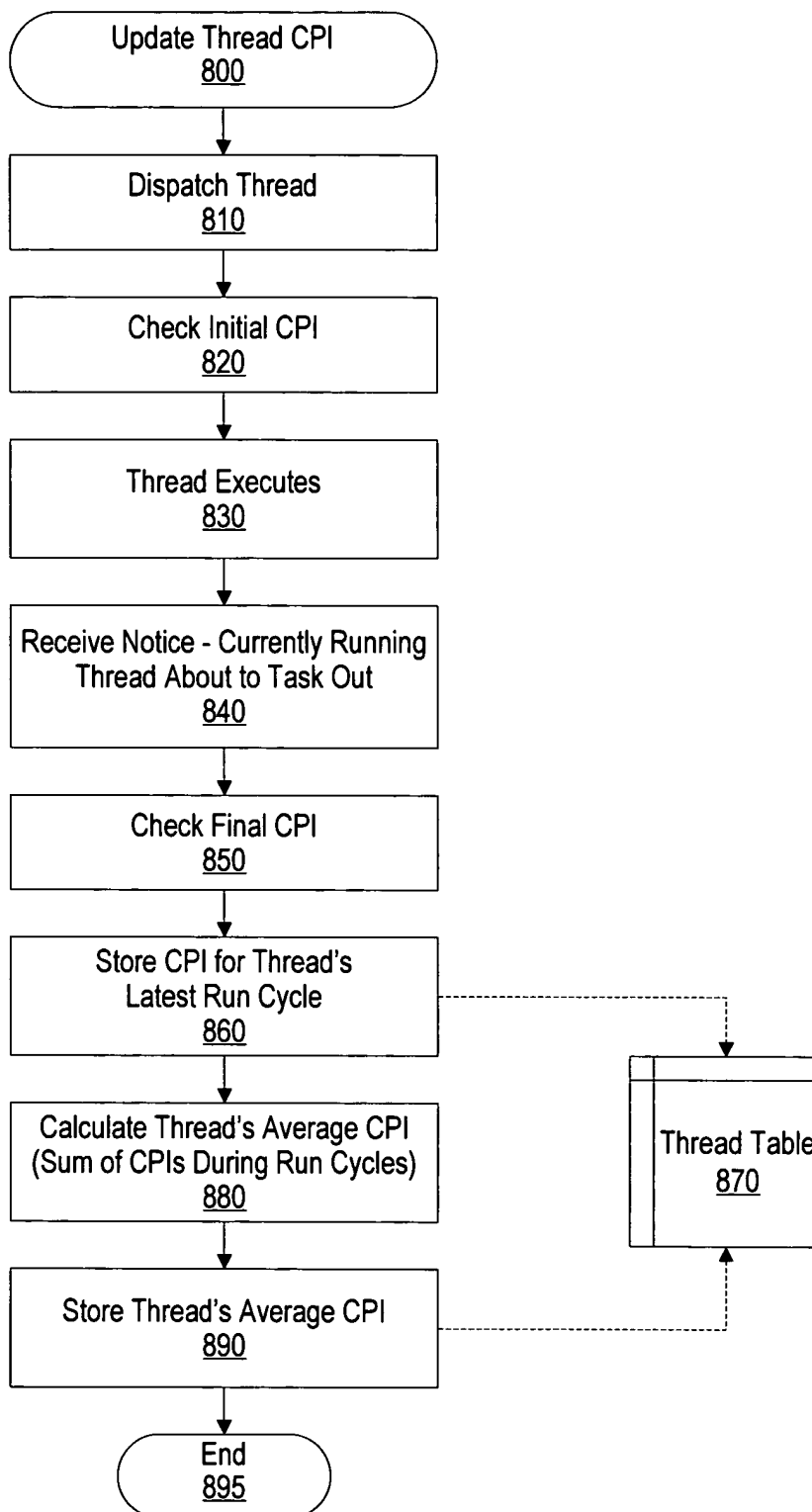
**Figure 6**

## System and Method for CPI Load Balancing in SMT Environments

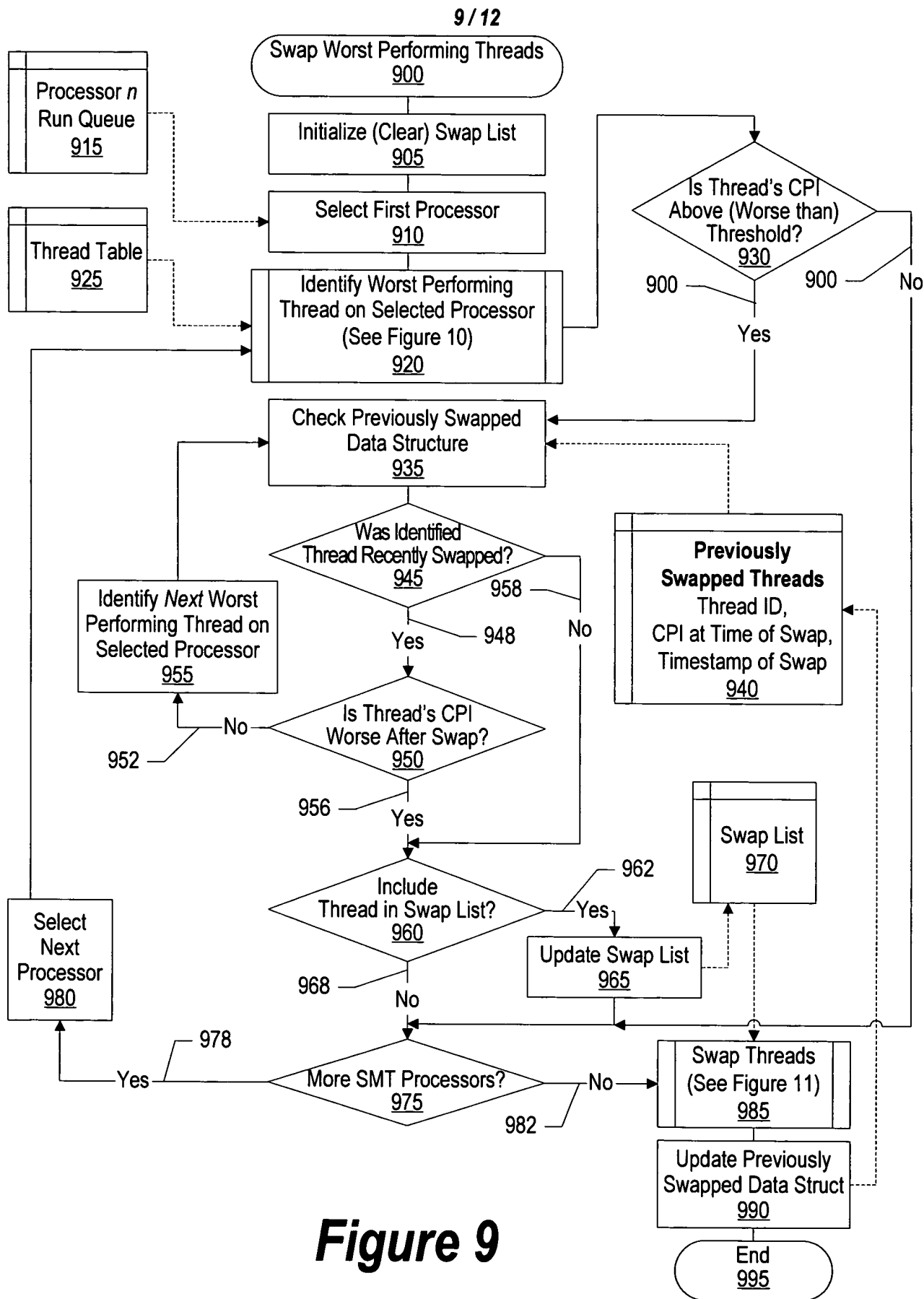
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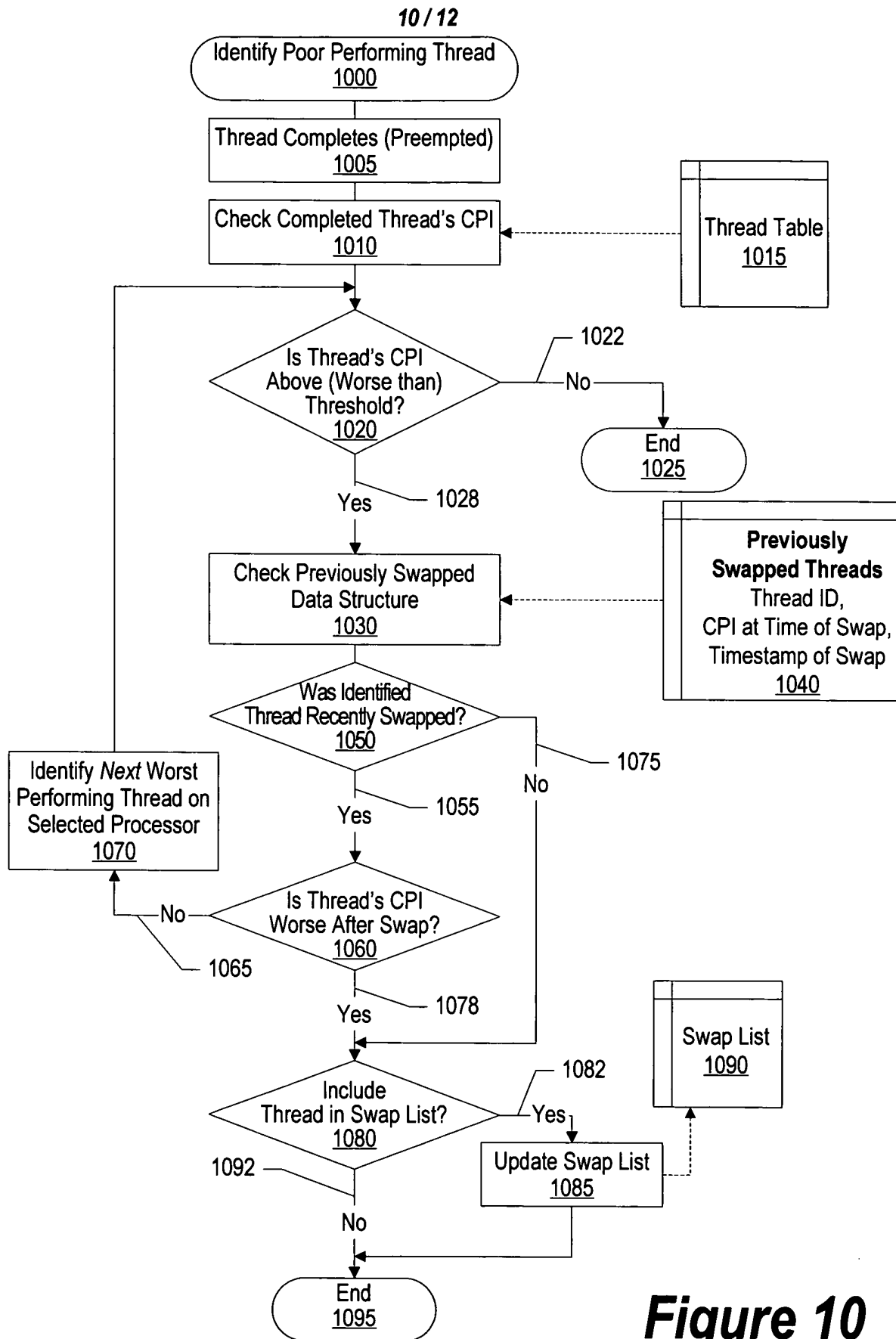
**Figure 7**

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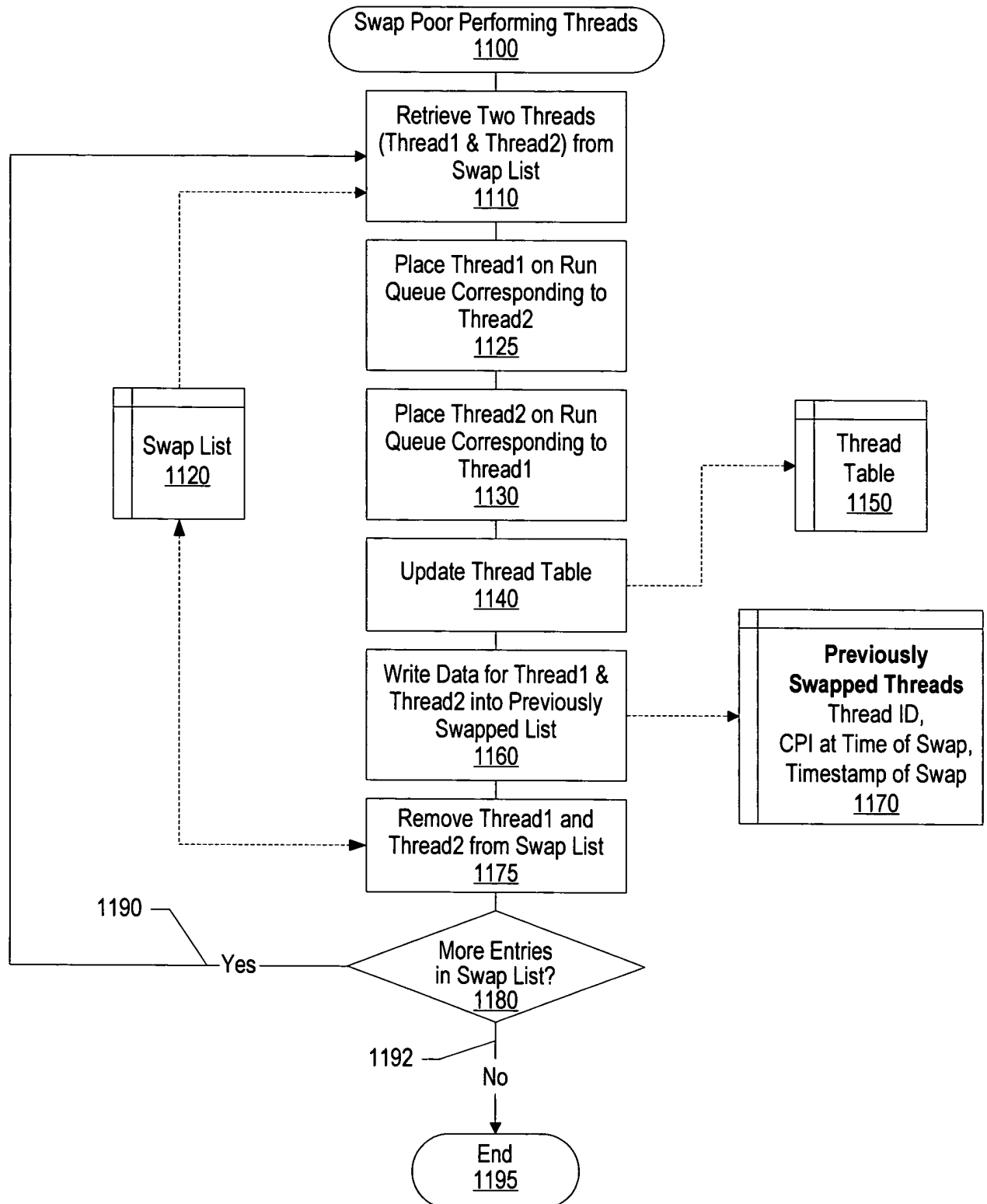
**Figure 8**



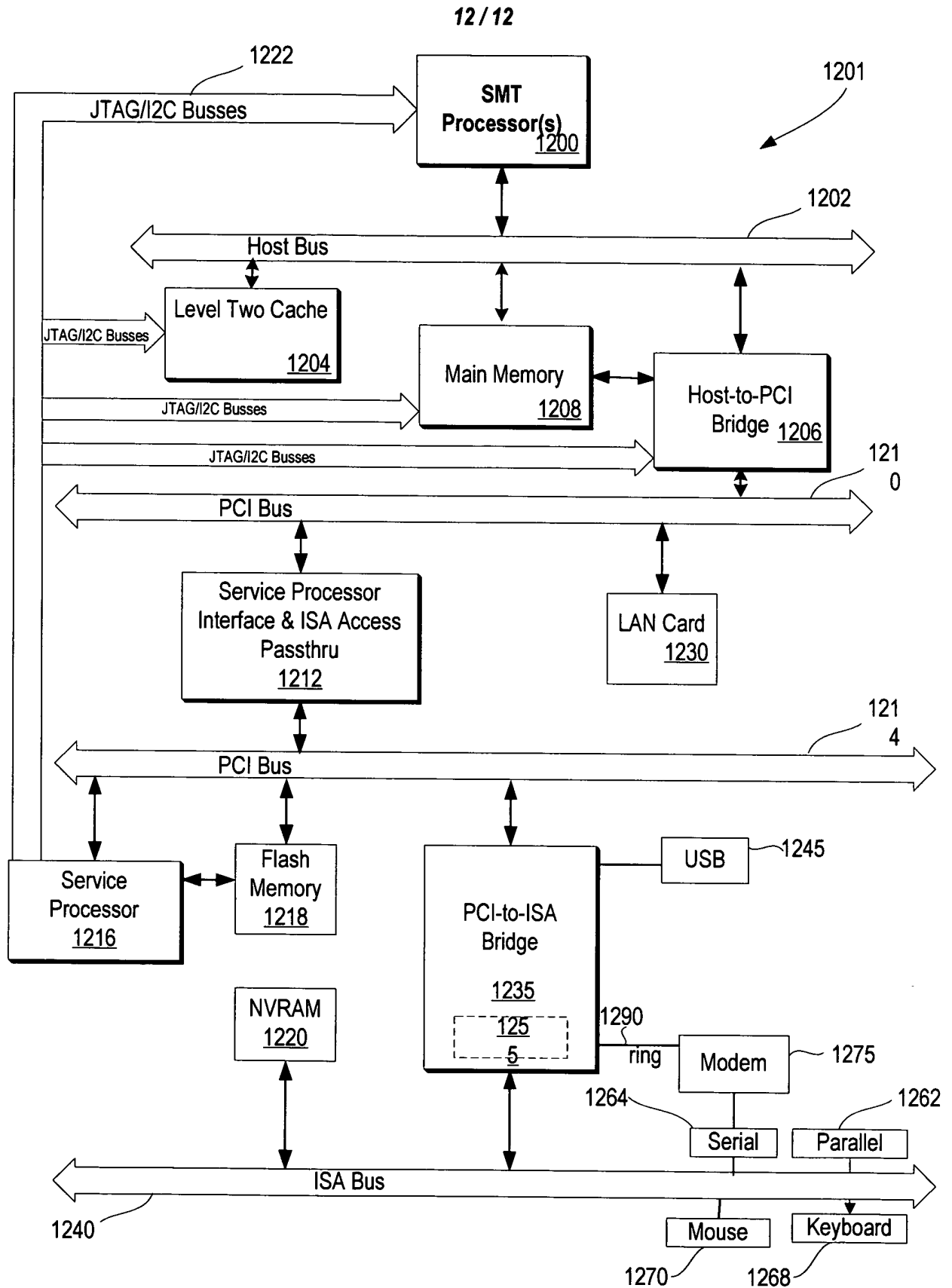
**Figure 9**



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**Figure 11**

## System and Method for CPI Load Balancing in SMT Environments

**Figure 12**